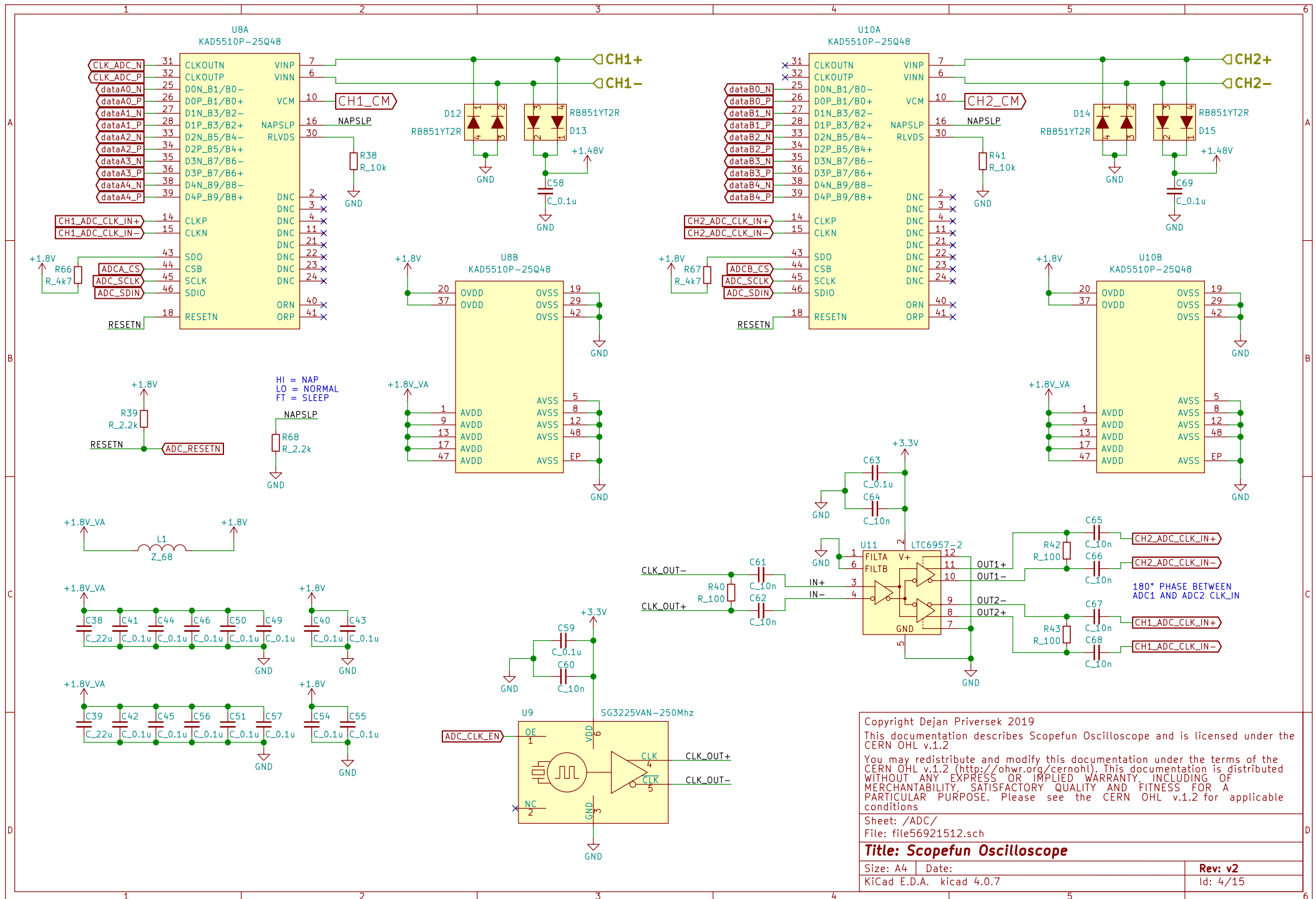


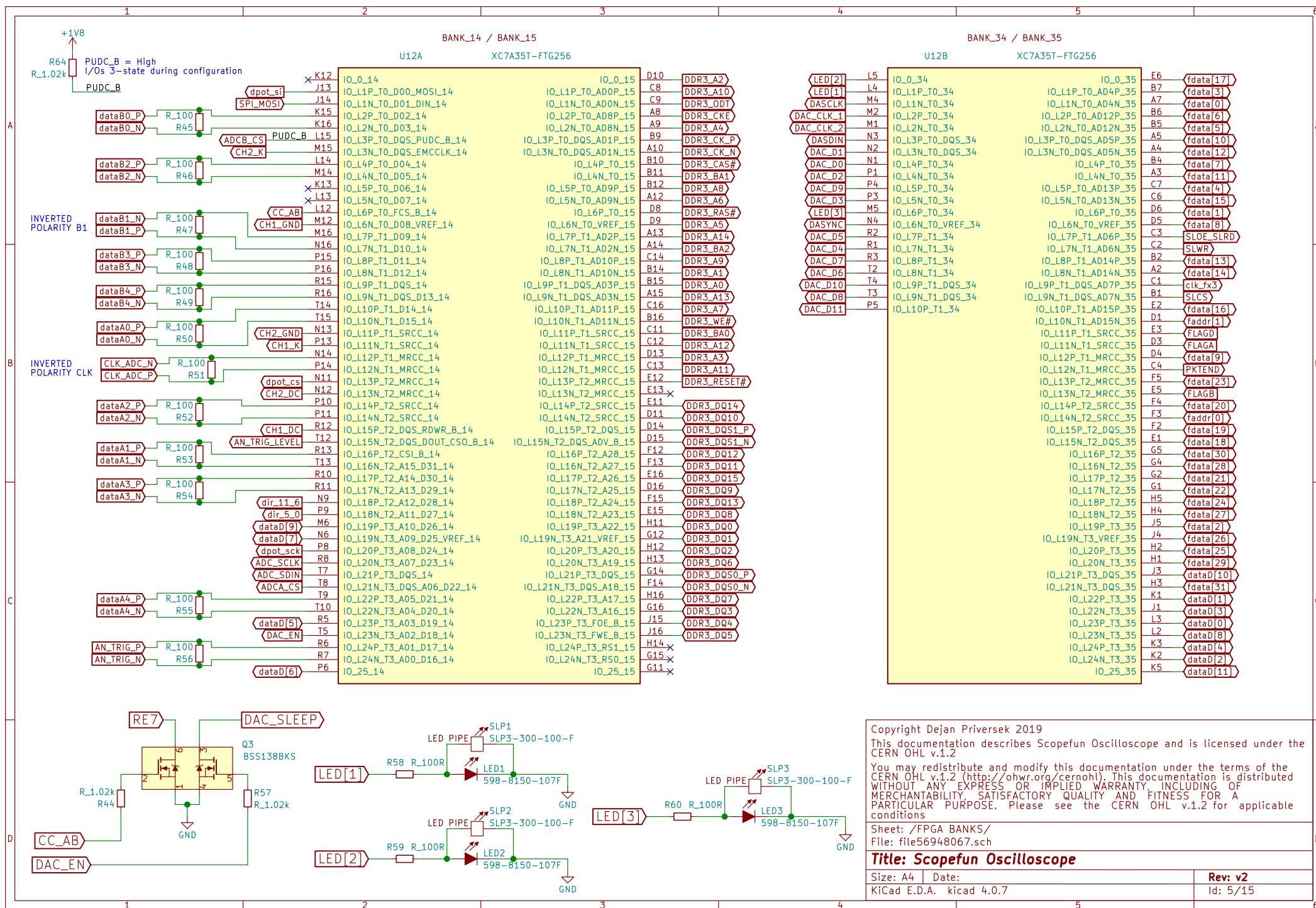
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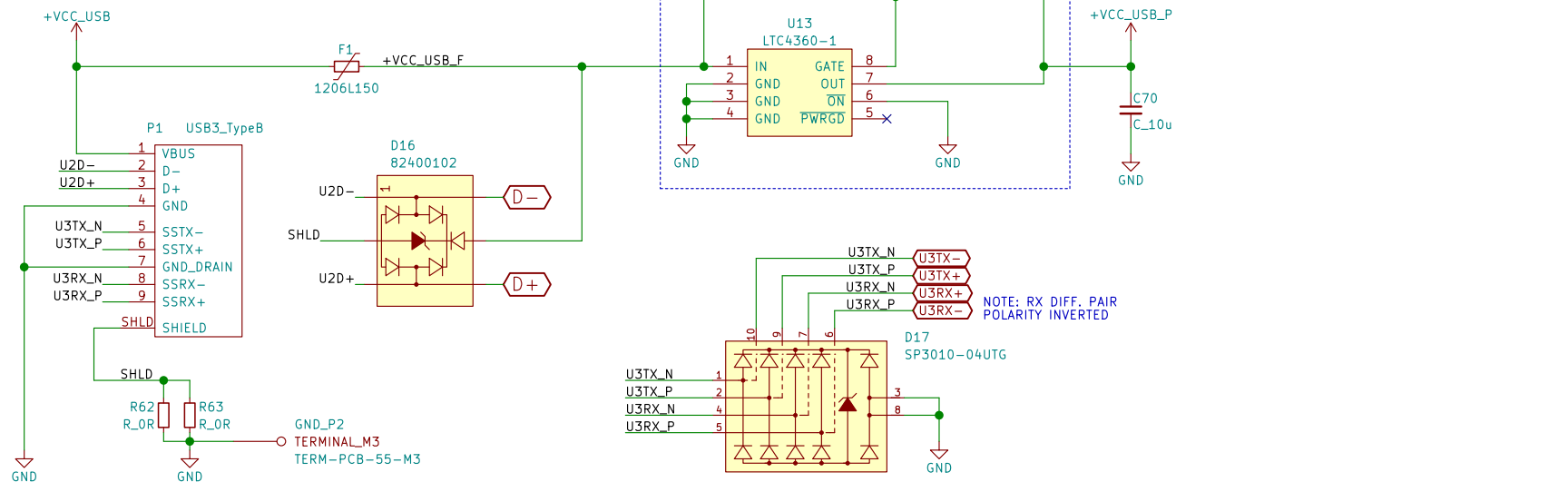
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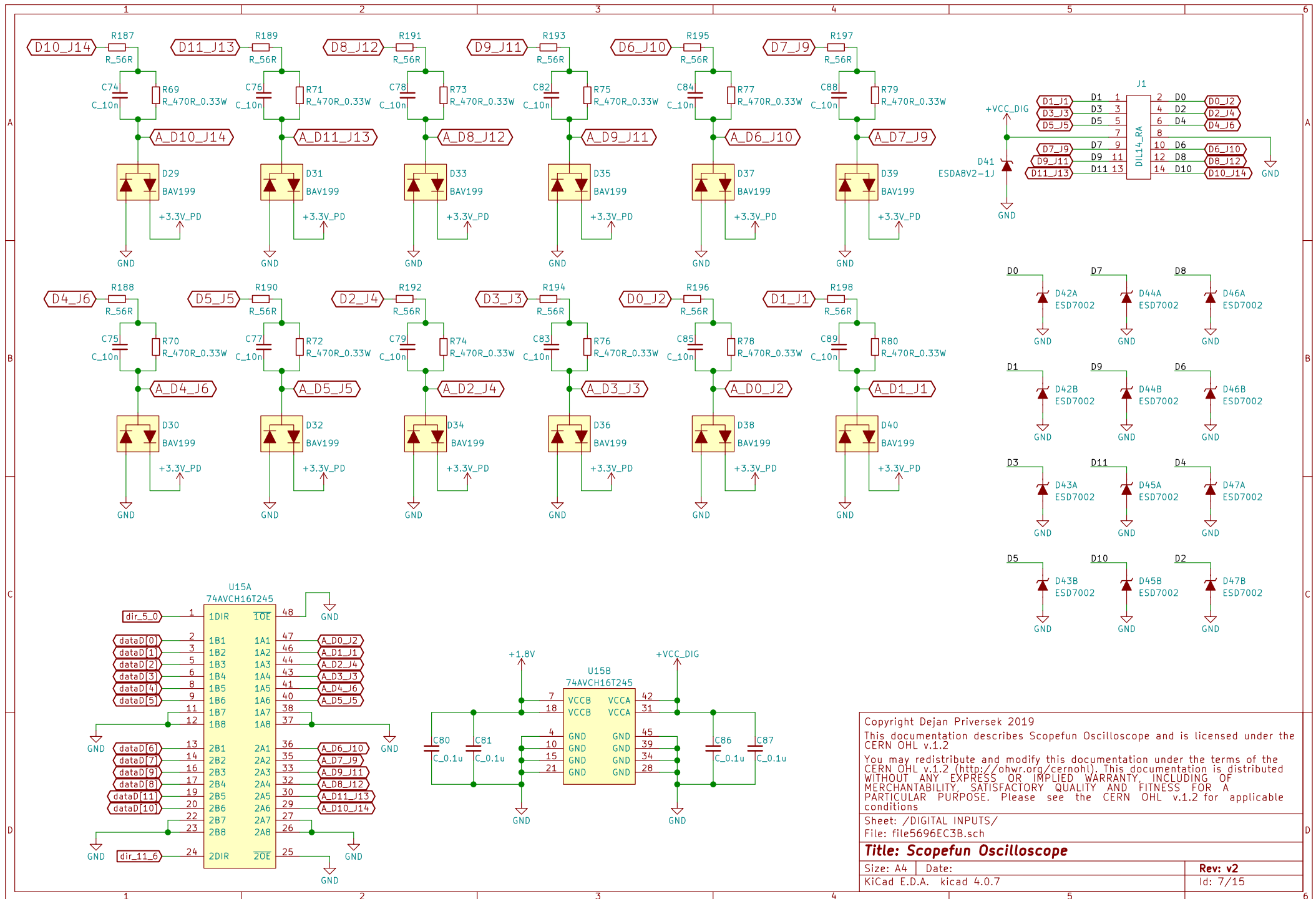
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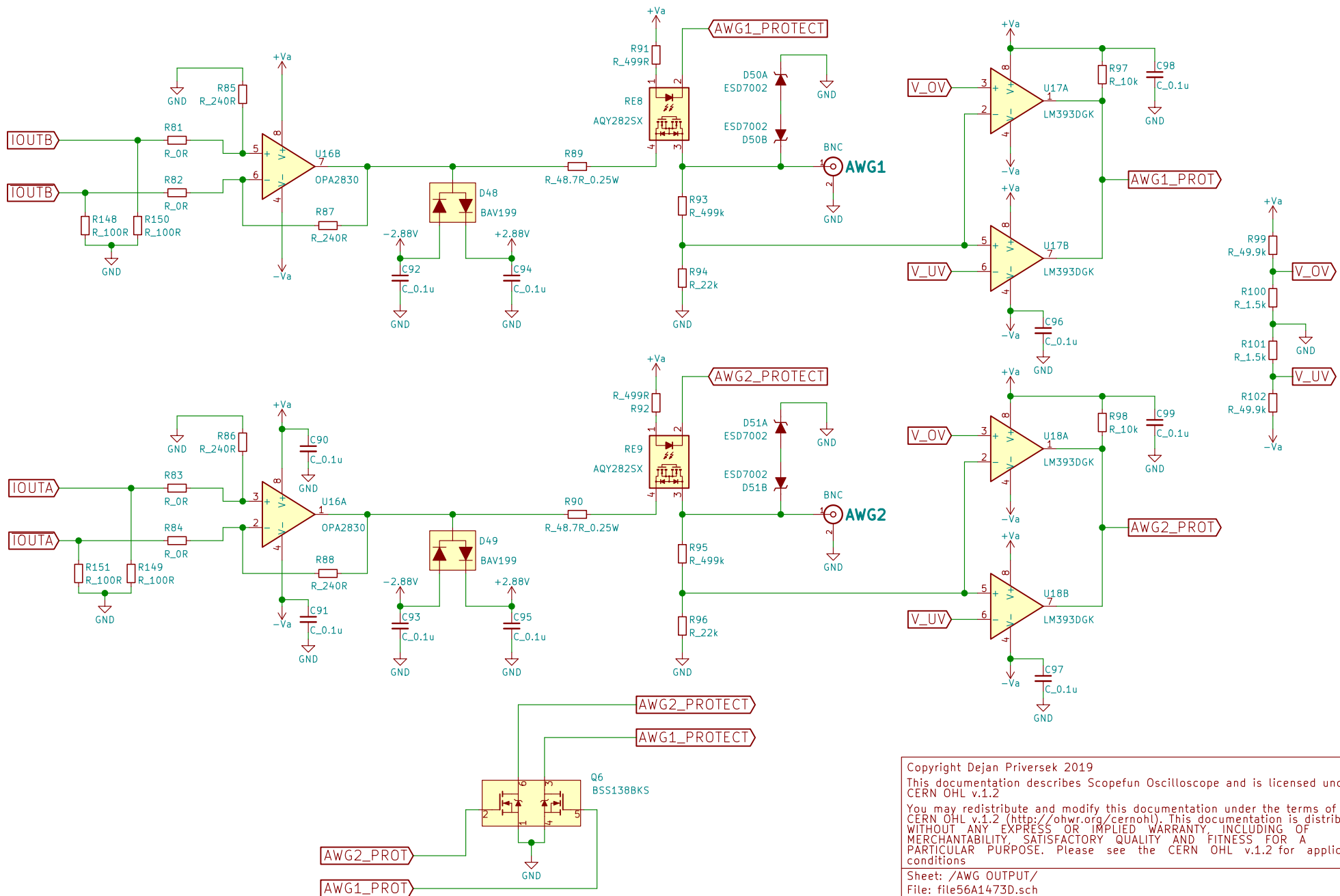
Size: A4	Date:	Rev: v2
KiCad E.D.A.	kiCad 4.0.7	Id: 3/15











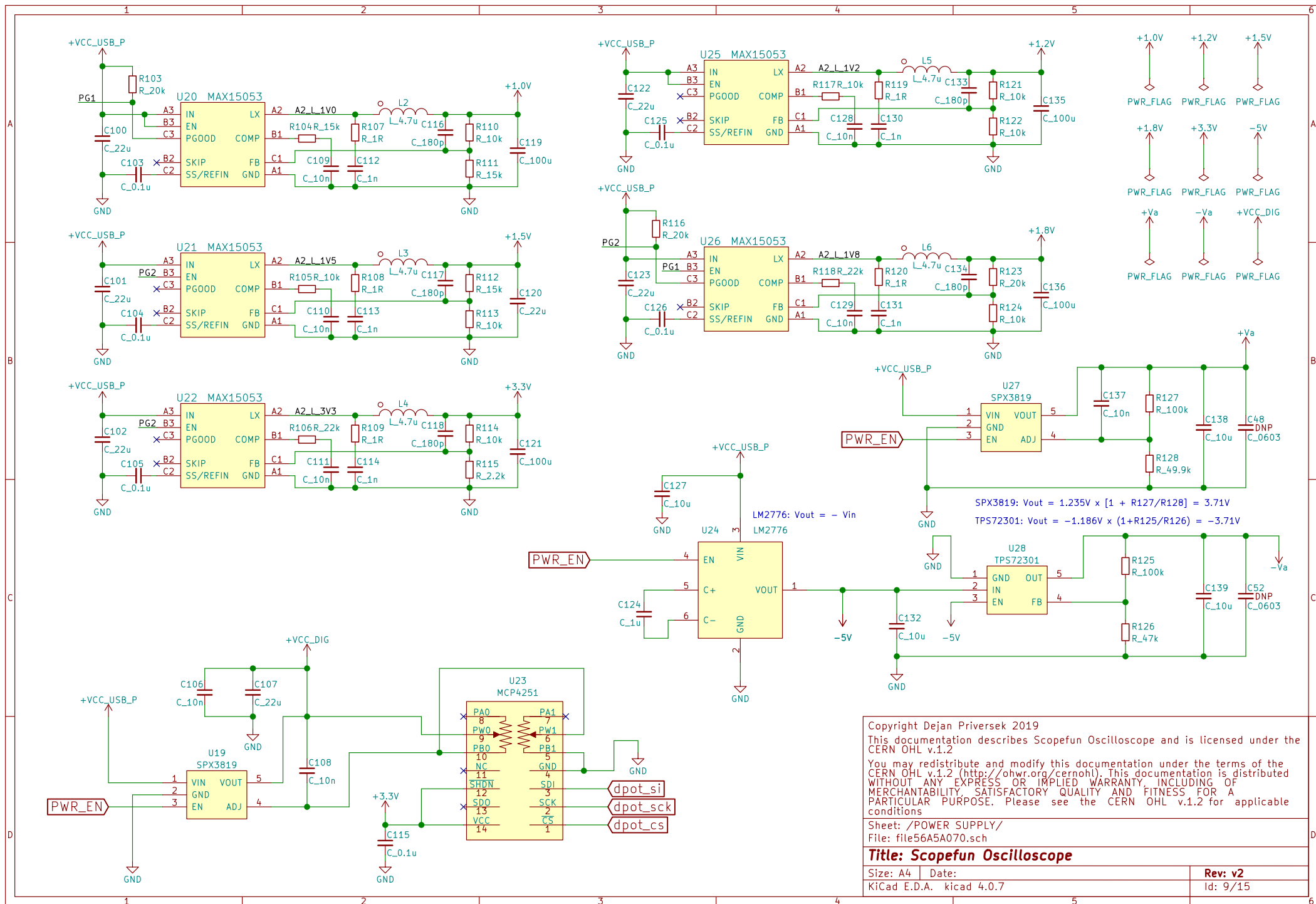
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Size: A4	Date:	Rev: v2
KiCad E.D.A.	kiCad 4.0.7	Id: 8/15





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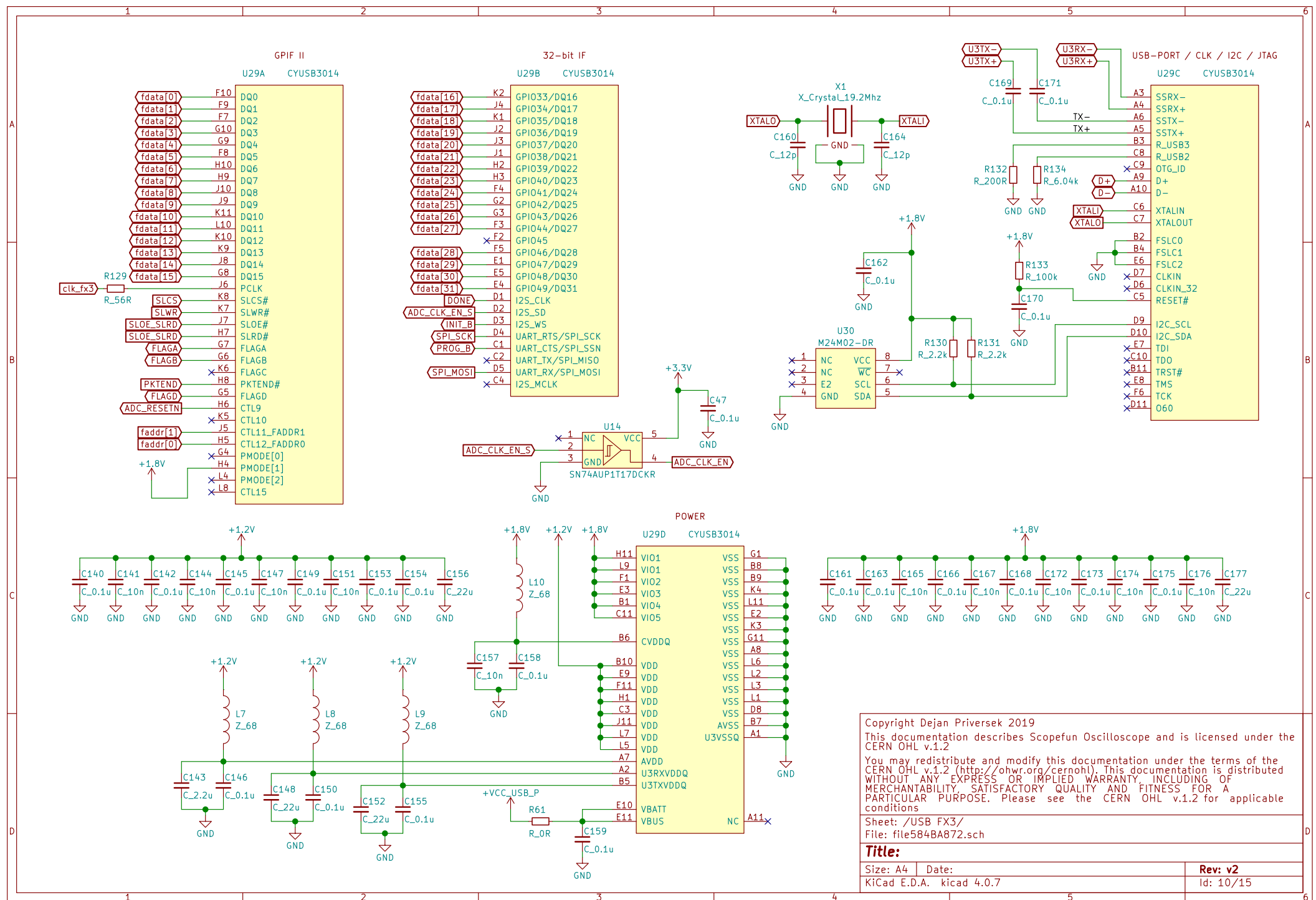
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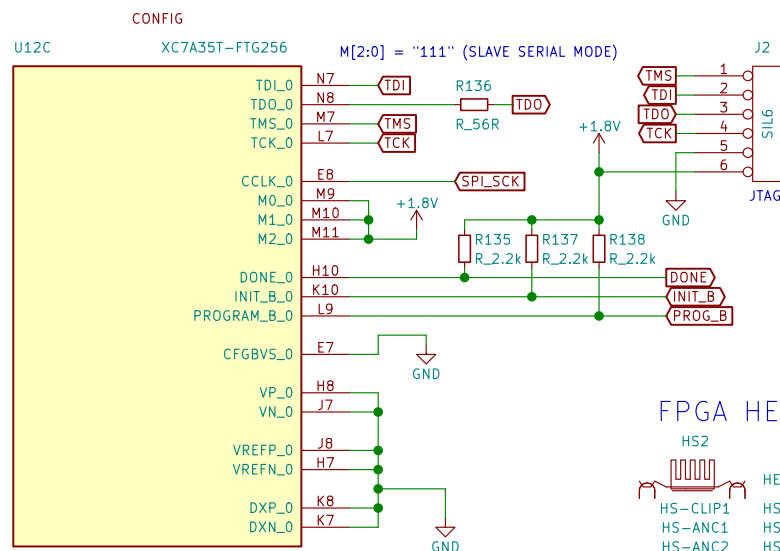
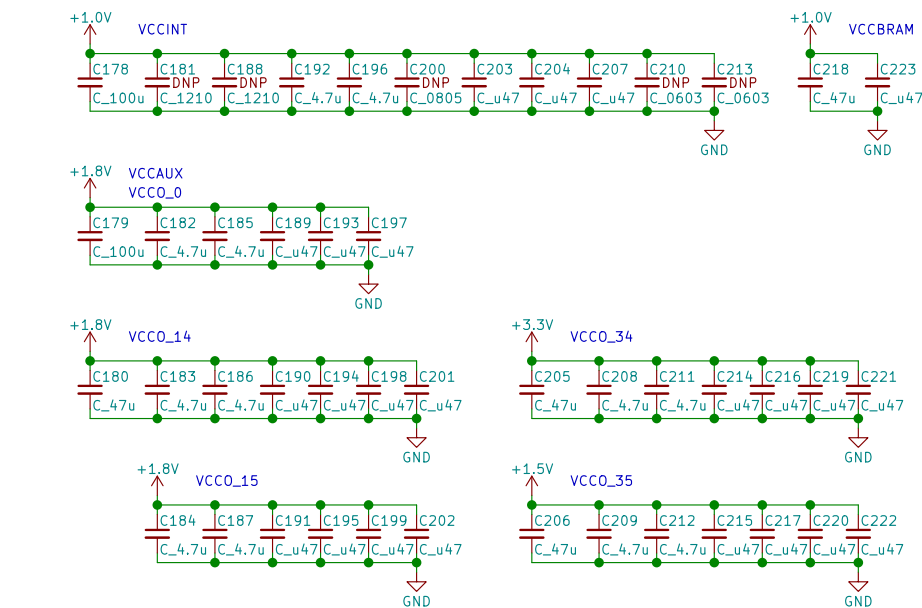
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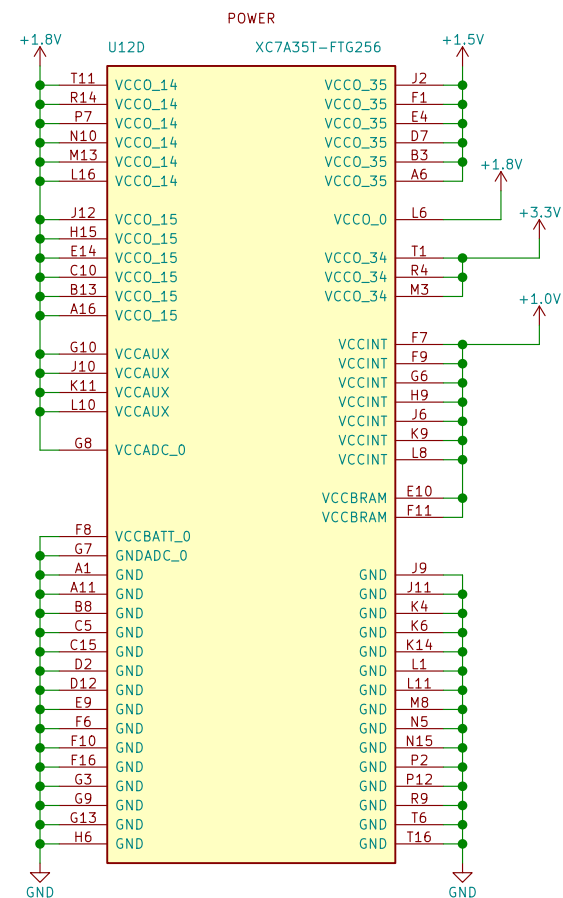
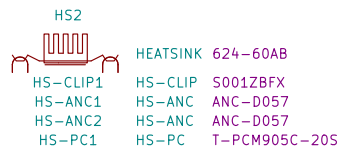
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## FPGA HEATSINK



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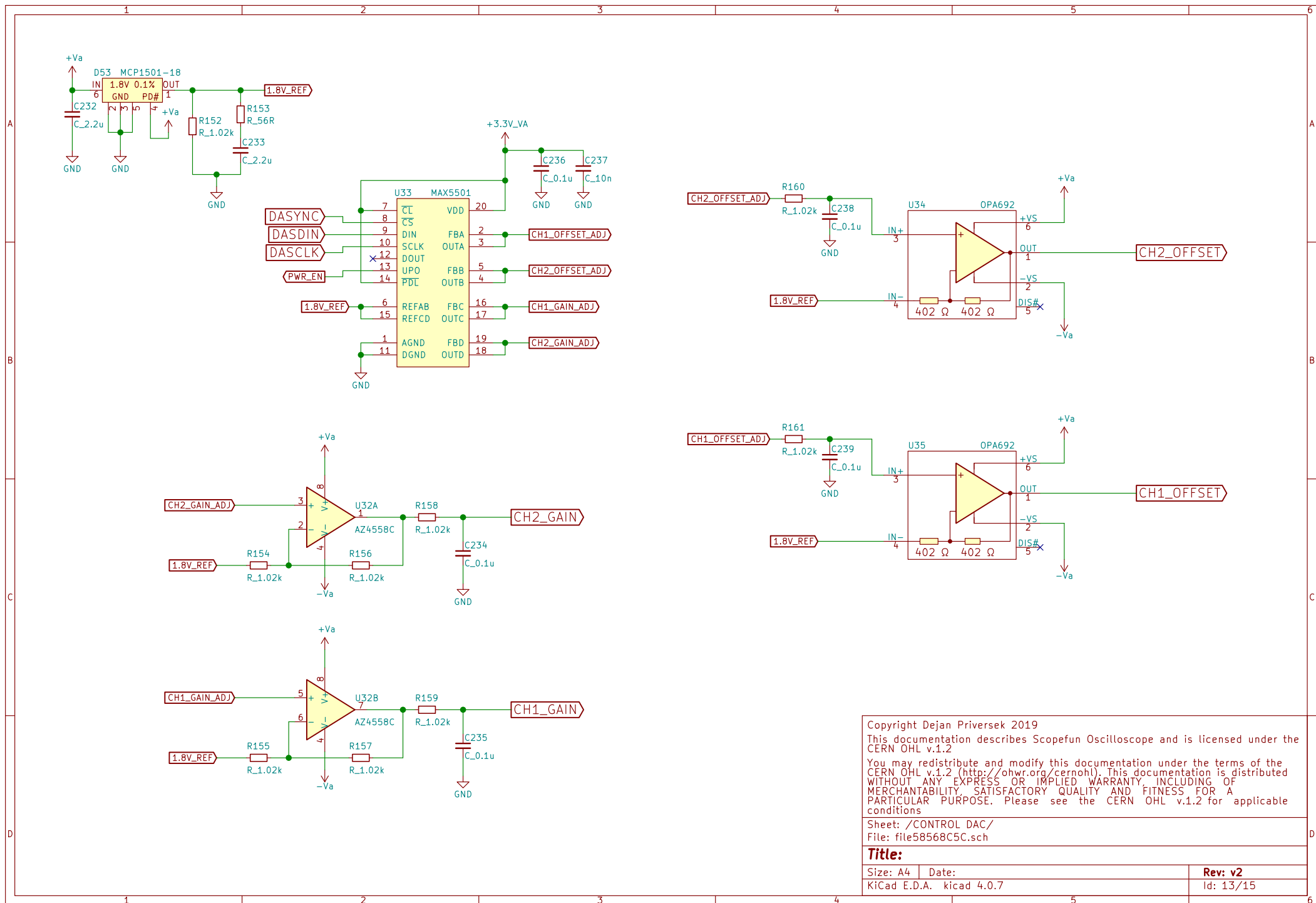
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Rev: v2  
Id: 11/15



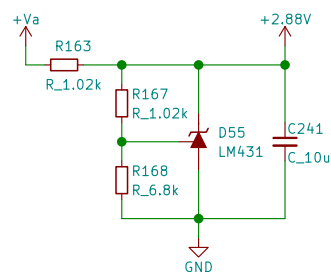
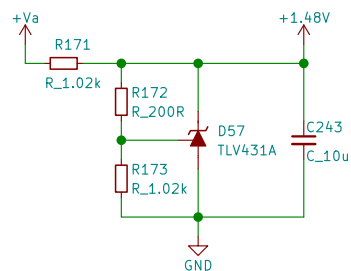
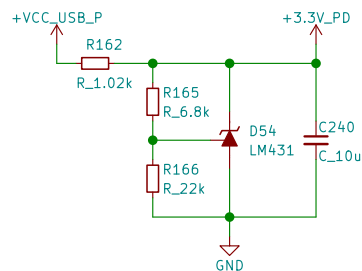


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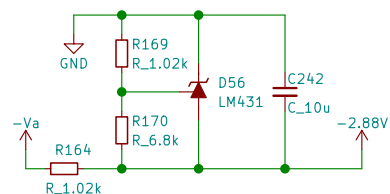
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Size: A4	Date:	Rev: v2
KiCad E.D.A.	KiCad 4.0.7	Id: 13/15



LM431:  $V_{out} = 2.5V * (1 + R_a/R_b)$



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